



METHODIST
COLLEGE OF ENGINEERING & TECHNOLOGY
(An UGC-AUTONOMOUS INSTITUTION)

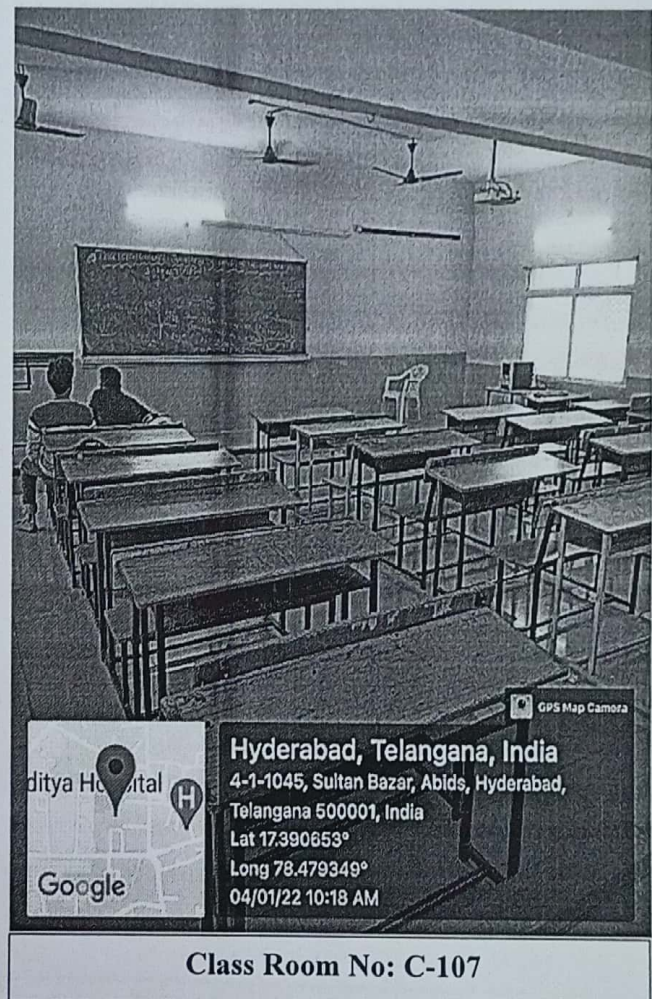
Estd : 2008

Accredited by NAAC with A+ and NBA
Affiliated to Osmania University & Approved by AICTE



Classroom:

A.Y.: 2021-22



[Signature]
Faculty In-charge

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Criterion In-charge



METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE FOR B.E V SEMESTER FOR THE ACADEMIC YEAR 2021-2022.

Class Time Table

Class: B.E V-SEM ECE-B

A.Y: 2021-22

W.E.F:- 08-11-2021

LH:- C-107

	I	II	III		IV	V	VI	VII
	9:30-10:30	10:30 -11:30	11:30-12:30	12:30-1:15	1:15-2:15	2:15-3:15	3:15-4:15	4:15-5:15
MON	AWP	ACS	MPMC		AC	AC	DSP	MP/RC
TUE	DSP	AWP	AC					

Class Time Table





METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE FOR B.E VI-SEMESTER FOR THE ACADEMIC YEAR 2021-2022.
Class Time Table

Class: B.E VI-SEM ECE-B

A.Y: 2021-22

W.E.F:- 28-03-2022

LH:-C-107

	I 9:30-10:30	II 10:30 -11:30	III 11:30- 12:30	12:30- 1:15	IV 1:15-2:15	V 2:15- 3:15	VI 3:15-4:15	VII 4:15-5:15
MON	DM	EMI	IOT		DC	DCCN LAB-B2/DSDV LAB-B1		MP/RC
TUE	EMI	DCCN	IOT		DSDV	DCCN	LIB	MP/RC
WED	DSDV LAB-B2/DC LAB -B1		DSDV		EMI	IOT	SPORTS	MP/RC
THU	DM	DM	DSDV		DC	DCCN	INT	MENTOR
FRI	DCCN LAB-B1/DC LAB-B2		DC		DC	DCCN	LIB	MENTOR
SAT	DSDV	EMI	DM		IOT	FOSS	FOSS	MENTOR

***(T) – Tutorial*(A) – Additional Class*(RC) – Remedial Class *(MP) – Mini Projects**

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
PC601EC	Digital Communication	Mrs. O. Ameena	LIB	LIBRARY	Mr.Maharishi Sanand
PC602EC	Digital system Design with Verilog	Mr. M.Mahesh Babu	SPORTS	SPORTS	Mr.P .Ramesh babu
PC603EC	Data Communication and computer networking	Mr.T .Venugopal	MENTOR	MENTORING	Dr. John William Care Medithe/sameed shaik/O.Ameena
PC604EC	Electronic Measurements and Instrumentation	Dr.K.Padmavathi	FOSS	FREE OPEN SOURCE SOFTWARE	
PC674EC	IoT Sensors	Mrs. Zeenath	INT	INTERNET	
OE 601CE	Disaster Management	Mr. Bharath naik	RC	Remedial Class	
PC651EC	Communication Lab	Mrs. O. Ameena(B-1&B-2)/Mr.CH.Suresh(B-1)/Mr.T.Sravankumar(B-2)			
PC652EC	DCCN Lab	Mrs. Zeenath(B-2)/ Mr.T .Venugopal(B-1)			
PC653EC	Digital system Design with Verilog Lab	Mr.M.Mahesh Babu(B-1&B2)/Dr.K.Padmavathi(B-2)/ Mr.Maharishi Sanand-B1	C-I	Class Incharge	Mr.M.Mahesh Babu

Time Table Coordinator

Head of the Department

Principal

